### IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Attv Dkt. 829-618

C#

2826

RESPONSE UNDER R **EXPEDITED HANDLING PROCED** 

In re Patent Application of

OCT 2 7 2005

YONEMARU, M.

Serial No. 10/720,764

Filed: November 25, 2003

Title: FOR SAME Examiner: Dickey, T.

TC/A.U.

Date: October 27, 2005

SEMICONDUCTOR INTEGRATED LOGIC CIRCUIT AND FABRICATION METHOD

M#

## Mail Stop AF

Commissioner for Patents .P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

#### RESPONSE/AMENDMENT/LETTER

This is a response/amendment/letter in the above-identified application and includes an attachment which is hereby incorporated by reference and the signature below serves as the signature to the attachment in the absence of any other signature thereon.

#### Fees are attached as calculated below:

Total effective claims after amendment 23 minus highest number x \$50.00 \$0.00 (1202)/\$0.00 (2202) \$ previously paid for 23 (at least 20) =

minus highest number Independent claims after amendment

x \$200.00 \$0.00 (1201)/\$0.00 (2201) \$ previously paid for 3 (at least 3) =

If proper multiple dependent claims now added for first time, (ignore improper); add

\$360.00 (1051)/\$180.00 (2051) \$

Petition is hereby made to extend the current due date so as to cover the filing date of this

paper and attachment(s) One Month Extension \$120.00 (1251)/\$60.00 (2251)

Two Month Extensions \$450.00 (1252)/\$225.00 (2252)

Three Month Extensions \$1020.00 (1253/\$510.00 (2253)

Four Month Extensions \$1590.00 (1254/\$795.00 (2254) \$

\$130.00 (1814)/\$65.00 (2814) \$ Terminal disclaimer enclosed, add

☐ Statement filed herewith Applicant claims "small entity" status.

\$180.00 (1806) \$ Rule 56 Information Disclosure Statement Filing Fee

\$ Assignment Recording Fee \$40.00 (8021)

**TOTAL FEE ENCLOSED \$** 0.00

\$

The Commissioner is hereby authorized to charge any deficiency, or credit any overpayment, in the fee(s) filed, or asserted to be filed, or which should have been filed herewith (or with any paper hereafter filed in this application by this firm) to our Account No. 14-1140. A duplicate copy of this sheet is attached.

901 North Glebe Road, 11th Floor Arlington, Virginia 22203-1808 Telephone: (703) 816-4000 Facsimile: (703) 816-4100

JAR:caj

Other:

**NIXON & VANDERHYE P.C.** 

By Atty: Joseph A. Rhoe, Reg No. 37,515

Signature:

# IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Indie Patent Application of

YONEMARU, M.

Atty. Ref.: 829-618; Confirmation No.

Appl. No. 10/720,764

TC/A.U. 2826

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Examiner: Dickey, T.

For: SEMICONDUCTOR INTEGRATED LOGIC CIRCUIT AND FABRICATION METHOD

FOR SAME

\* \* \* \* \* \* \* \* \* \*

October 27, 2005

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

## **AMENDMENT AFTER FINAL**

Responsive to the Official Action dated July 27, 2005, please amend the above-identified application as follows: